

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-8. (Canceled)

9. (Currently amended): A ceramic circuit board comprising:

 a ceramic substrate having a through hole;

 a metal column arranged within the through hole; and

 metal circuit plates attached to both surfaces of the ceramic substrate in such a way as to stop up the through hole,

 wherein the metal circuit plates attached to both surfaces of the ceramic substrate are connected to each other by the metal column,

 wherein, between an inner wall surface of the through hole and an outer wall surface of the metal column is secured a space defining a cavity, wherein the cavity is free from any material, and wherein the space defining the cavity exists along the entire length of the metal column,

and wherein a brazing material is provided on each end of the metal column, and the brazing material is not connected to the ceramic substrate.

10. (Original): The ceramic circuit board of claim 9,

 wherein the metal circuit plate is made of copper or aluminum.

11. (Original): The ceramic circuit board of claim 9,

 wherein the metal column is made of copper or aluminum.

12-16. (Canceled)

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17. (Previously presented): The ceramic circuit board of claim 9,
wherein a distance between the inner wall surface of the through hole
and the outer wall surface of the metal column is in a range of 30 to 200 μm .

18. (Previously presented): The ceramic circuit board of claim 9,
wherein the metal circuit plate has its surface plated with a layer
made of nickel.

19. (Previously presented): The ceramic circuit board of claim 18,
wherein the plate layer is made of a nickel-phosphorous amorphous
alloy containing phosphorous in an amount of 8 to 15 wt%.

20. (Previously presented): The ceramic circuit board of claim 18,
wherein the plate layer is 1.5 to 3 μm thick.

21. (Previously presented): The ceramic circuit board of claim 9,
wherein the metal column has a diameter of 200 μm or above.